

Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

PRODUCT SPECIFICATION

Model No: CSST-NLANWGA95-K5X

Descriptions:

: White SMD LED

: PLCC2 3.5x2.8 x0.8mm

Emitting Color : Neutral WhiteEncapsulation : Silicone Resin









CUSTOMER APPROVED SIGNATURES	APPROVED BY	CHECKED BY	PREPARED BY
	2023, 03, 16 Leon W All rights reserved	2023, 03, 16 ZXY All rights reserved	2023. 03. 16 GY All rights reserved

OPTO PLUS TECHNOLOGIES CO.,LTD

Address: No.696, Yangming North Rd, Shao Xing

City, Zhe Jiang Province, P.R. China

Tel: 86-575-88623888 Fax: 86-575-88623112

http://www.csbright.com



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

■ Features –

- 1. High luminous intensity using InGaN dice Technology
- 2. High reliability package due to silicone encapsulation
- 3. Wide viewing angle at 120°
- 4. RoHS Compliant
- 5. Compatible Lead-Free Reflow Soldering process
- 6. JEDEC MSL 2a

Applications –

- 1. General lighting
- 2. Architecture and entertainment lighting
- 3. Electronic signs and signals
- 4. Interior automotive lighting

Absolute Maximum Rating Polarity—

(Ta=25°C)

Parameter	Symbol	Rating	Unit	
Power Dissipation	Pd	500	mW	
Forward Current	l _F	150	mA	
Peak Forward Current*	I _{FP}	300	mA	
Reverse Voltage*	V_{R}	5	V	
LED Junction Temperature*	Tj	110	$^{\circ}\!\mathbb{C}$	
Operating Temp.	T_{opr}	-40~ +85	$^{\circ}\!\mathbb{C}$	
Storage Temp.	T_{stg}	-40 ~ +100 °C		
Soldering Town	+	Reflow Soldering : 260°C for 10 sec		
Soldering Temp.	T _{sol}	Hand Soldering : 350°C for 3 sec		

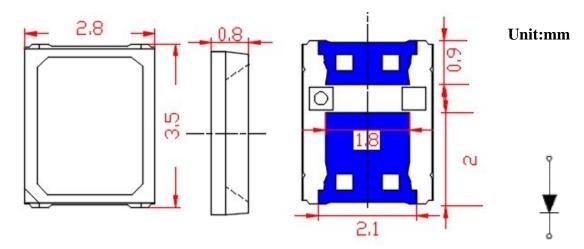
Notes:

- 1. Pulse width \leq 0.1 msec, duty \leq 1/10
- 2. Proper current rating must be observed to maintain junction temperature below the maximum at all the time.
- 3. The device can not operated under continuous reverse voltage.



Spec. No.	PS-ST-NLANWGA95-K5X			
Rev.	D			

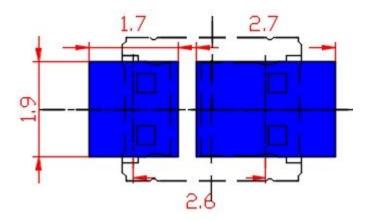
■ Package Outline Dimensions –



Notice: Tolerance of measurement of Dimension: ±0.2mm

■ Recommended Soldering Pad Pattern

Unit:mm



■ Electrical / Optical Characteristics -

(Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward Voltage	V _F		3.1		V	IF=150mA
Luminous Flux	Фν		58		lm	
Viewing Angle	2 <i>θ</i> 1/2		120		deg	
Color Rendering Index	Ra	90	95			
Reverse Current	I R			10	μ A	VR=5V

Page: 3/17



Spec. No.	PS-ST-NLANWGA95-K5X			
Rev.	D			

■ Luminous Flux Rank Limits (I_F = 150mA)

unit: Im

Luminous Flux Part No.	P500	P550	P600	P650
CSST-NLANWGA95-K5X	50-55	55-60	60-65	65-70

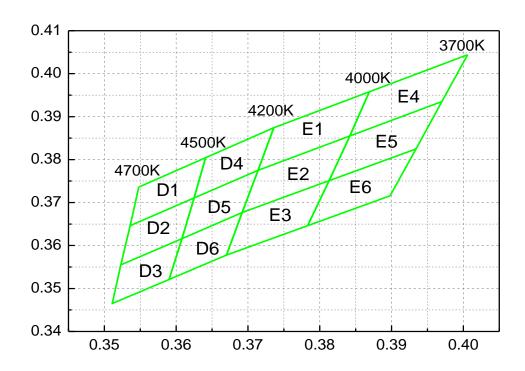
Notice: Tolerance of measurement of Luminous Flux: ±7%

■ Forward Voltage Rank Limits –

V _F Rank	Min	Max	Unit	Condition
V28	2.8	2.9		
V29	2.9	3.0		1 450m A
V30	3.0	3.1	V	
V31	3.1	3.2		$I_F = 150mA$
V32	3.2	3.3		
V33	3.3	3.4		

Notice: Tolerance of measurement of Forward Voltage: ±0.1V

\blacksquare Color Rank Limits ($I_F = 150 \text{mA}$)



Page: 4/17



Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

Rank D Color Temperature:4500~4700K							
D	D1 D2 D3						
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y		
0.3548	0.3736	0.3536	0.3646	0.3523	0.3555		
0.3536	0.3646	0.3523	0.3555	0.3511	0.3465		
0.3625	0.3711	0.3608	0.3616	0.3590	0.3521		
0.3641	0.3804	0.3625	0.3711	0.3608	0.3616		

Rank D Color Temperature:4200∼4500K							
D	D4 D5 D6						
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y		
0.3641	0.3804	0.3625	0.3711	0.3608	0.3616		
0.3625	0.3711	0.3608	0.3616	0.359	0.3521		
0.3714	0.3775	0.3692	0.3677	0.3670	0.3578		
0.3736	0.3874	0.3714	0.3775	0.3692	0.3677		

Rank E Color Temperature:4000∼4200K							
E1 E2 E3							
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y		
0.3736	0.3874	0.3714	0.3775	0.3692	0.3677		
0.3714	0.3775	0.3692	0.3677	0.3670	0.3578		
0.3842	0.3855	0.3813	0.3751	0.3783	0.3646		
0.3869	0.3958	0.3842	0.3855	0.3813	0.3751		

Rank E Color Temperature:3700∼4000K							
E4 E5 E6							
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y		
0.3869	0.3958	0.3842	0.3855	0.3813	0.3751		
0.3842	0.3855	0.3813	0.3751	0.3783	0.3646		
0.3970	0.3935	0.3934	0.3825	0.3898	0.3716		
0.4006	0.4044	0.397	0.3935	0.3934	0.3825		

Notice:Tolerance of measurement of Color Coordinates: ±0.01



Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

■ Typical Electrical / Optical Characteristics Curves -

(Ta = 25℃ Unless Otherwise Noted)

Figure1 Forward Current VS Ambient Temperature

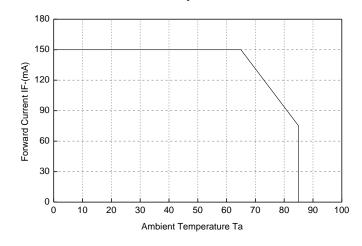


Figure 2. Forward Current VS. Forward Voltage

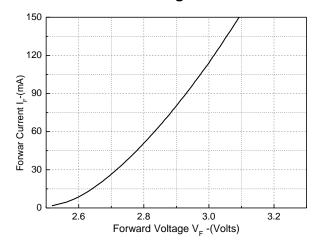
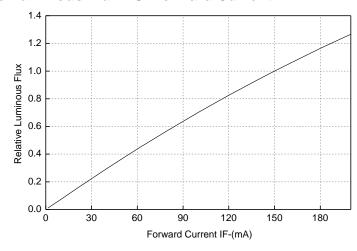


Figure 3. Relative Luminous Flux VS. Forward Current





Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

Figure 4. CIE X VS. Forward Current

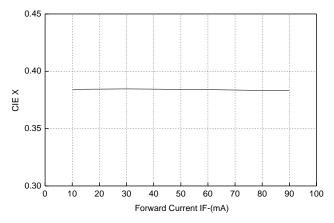


Figure 5. CIE Y VS. Forward Current

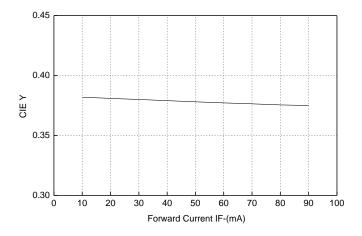
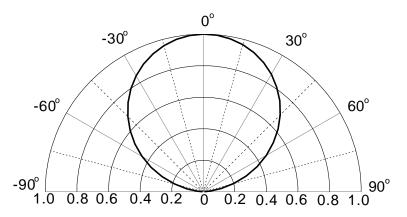


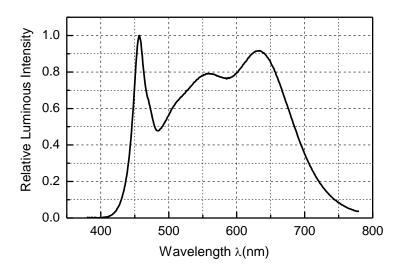
Figure 6. Relative Luminosity VS. Radiation Angle





Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

Figure 7. Relative Spectral Power Distribution VS. Wavelength

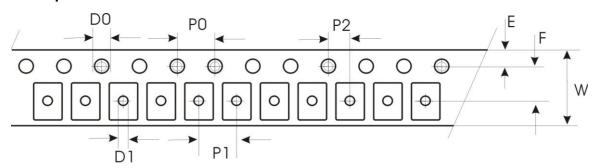


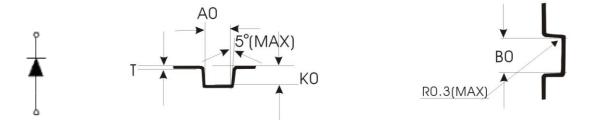


Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

■ Package –

1. Tape Dimension





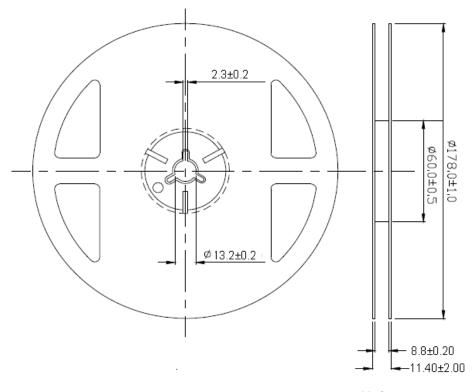
Unit: mm

Symbol	A0	В0	K0	P0	P1	P2	т
Spec	3.0±0.1	3.8±0.1	1.0±0.1	4.0±0.1	4.0±0.1	2.00±0.1	0.22±0.05
Symbol	E	F	D0	D1	W	P0	
Spec	1.75±0.10	3.50±0.05	1.5±0.1	1.0±0.1	8.0±0.1	40.0±0.2	



Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

2. Reel Dimension

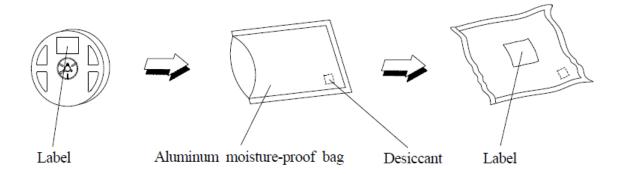


Unit: mm

Notice: (1) Quantity:4000PCS/Reel

(2)Tolerance unless mentioned is ±0.2mm

3. Packing Model





Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

■ Packing Amount

	Package Dime	nsion	Distribution of Total Mount				
Package Name			the layer	or box			Note
	Size	Unit	Amount	Unit	Amount	Unit	
Reel	8	mm	1	Reel	4000	Pcs	
Inner Box	265X235X78	mm	5	Reel	20000	Pcs	
Outer Box	540x260x170	mm	4	Inner	80000	Pcs	

Page: 11/17



Spec. No.	PS-ST-NLANWGA95-K5X		
Rev.	D		

Soldering Characteristics

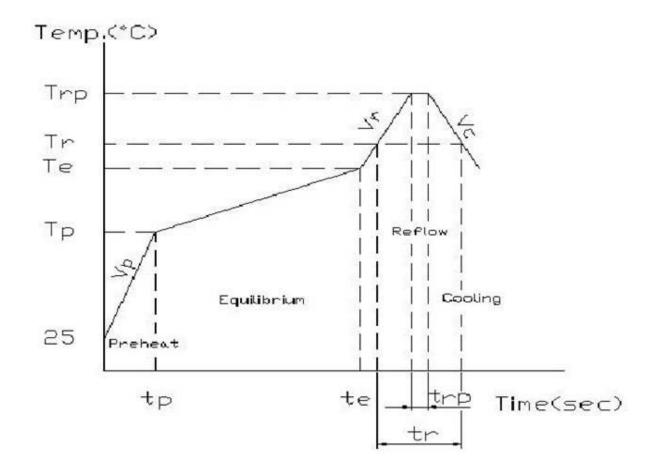
IR-reflow Condition (Pb free)

Area	Title	Symbol	Min	Max	Unit
	Ramp-up rate	Vp	1	5	°C/sec
(1)Preheat	temperature	Тр	150	_	°C
	time	tp	-	_	sec
	Ramp-up rate	Ve	ı	ı	°C/sec
(2)Equilibrium	temperature	Те	150	200	°C
	Time	te	60	120	sec
	Ramp-up rate	Vr	1	5	°C/sec
	temperature	Tr	220	I	°C
(3)Reflow	Time	tr	ı	60	sec
	Peak temperature	Trp	-	260	°C
	Peak time	trp	-	10	sec
(4)Cooling	Ramp-down rate	Vc	3	6	°C/sec

Page: 12/17



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D



Hand Soldering (Iron Condition)

Soldering Iron:30W Max

Temperature 350°C Max (iron tip 260°C Max)

Soldering Time:3 Seconds Max(Once)



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

Reliability Test Program/ Reliability Test Item

NO.	Test Item	Standard Test Method	Test Conditions	Test Duration	Failure Criteria	Units Failed/Tested
	Resistance to	JEITA ED-4701	Tsld=260°C, 10sec,reflows		4	0/00
1	Soldering Heat	300 301	Pretreatment30°C,70%,168hrs		#1	0/20
2	Solderability	JEITA ED-4701	Tsld=245±5°C,5sec.		# 0	0/00
	(Reflow Soldering)	300 303A	Lead-free Solder(Sn-3.0Ag-0.5Cu)		#3	0/20
3	Thermal Shock	JEITA ED-4701	-40°C~110°C 10min dwell,	100cycle	#1	0/20
	memai Shock	300 307	10sec transfer,	s	#1	0/20
4	Temperature Cycle		-40°C (30min) ~25°C (5min) ~	100cycle	#1	0/20
			110°C (30min) ∼25°C (5min)	s		0/20
5	High Temperature	JEITA ED-4701	Ta=110°C	1000hrs.	#1	0/20
	Storage	200 201	14-110 0	TOOOTIIS.	# I	0,20
6	Temperature	JEITA ED-4701	Ta=60°C, RH=90%	1000hrs.	#1	0/20
	Humidity	100 103		rooonis.	π Ι	0/20
7	Low Temperature	JEITA ED-4701	Ta=-40°C	1000hrs.	#1	0/20
	Storage	200 202		roooms.	π Ι	0/20
8	Room		Ta=25°C, If=150mA	1000 hrs.	#2	0/20
	Temperature					
9	Low Temperature		Ta=-40°C, If=150mA	1000hrs.	#2	0/20
	Operating life					

Page: 14/17



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

Failure Criteria

Criteria #	Items	Conditions	Failure Criteria
#4	Forward Voltage(VF)	If=150mA	>U.S.L.X1.1
#1	Luminous Intensity(IV)	If=150mA	<l.s.l.x0.7< td=""></l.s.l.x0.7<>
#0	Forward Voltage(VF)	If=150mA	>U.S.L.X1.1
#2	Luminous Intensity(IV)	If=150mA	<l.s.l.x0.5< td=""></l.s.l.x0.5<>
#3	Solderability		Less than 95% solder coverage

U.S.L.: Upper Specification limit L.S.L.: Lower Specification Limit

Page: 15/17



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

Handling of Silicone Resin LEDs-

Handling Indications

i. When handling the product, do not touch it directly with bare hands as it may contaminate the surface and affect on optical characteristics. In the worst cases, excessive force to the product might result in catastrophic failure due to package damage and/or wire breakage.



ii. When handling the product with tweezers, LEDs should only be handled from the side and make sure that excessive force is not applied to the resin portion of the product. Failure to comply can cause the resin portion of the product to be cut, chipped, delaminated and/or deformed, and wire to be broken, and thus resulting in catastrophic failure.





Pick and place

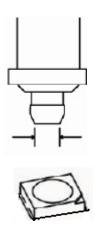
Recommended conditions: Outer nozzle>Φ2.5mm

Avoid direct contact to the encapsulant with picking up nozzle. Failure to comply might result in pick and place processes or damage to encapsulant. In the worst cases, catastrophic failure of the LEDs due to wire deformation and/or breakage.

Page: 16/17



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D



Storage –

Storage Conditions

A. Before opening the package:

The LEDs should be kept at $\leq 40^{\circ}$ C and $\leq 90\%$ RH. The LEDs should be used within a year. When storing the LEDs, moisture proof packaging with absorbent material (silica gel) is recommended.

B. After opening the package:

The LEDs should be kept at $\leq 30^{\circ}$ C and $\leq 60\%$ RH. The LEDs should be soldered within 672 hours (4 weeks) after opening the package. If unused LEDs remain, they should be stored in moisture proof packages, such as sealed containers with packages of moisture absorbent material (silica gel). It is also recommended to return the LEDs to the original moisture proof bag and to reseal the moisture proof bag again.

- If the moisture absorbent material (silica gel) has faded away or the LEDs have exceeded the storage time, baking treatment should be performed using the following conditions.
 - Baking treatment: more than 24 hours at 60 ± 5°C
- This product has silver plated metal parts that are inside and/or outside the package body. The silver plating becomes tarnished when being exposed to an environment which contains corrosive gases. Any LED with tarnished leads may lead to poor solderability and deterioration of optical characteristics. Please do not expose the LEDs to corrosive atmosphere during storage.
- After assembly and during use, silver plating can be affected by the corrosive gases emitted by components and materials in close proximity of the LEDs within an end product, and the gases entering into the product from the external atmosphere. The above should be taken into consideration when designing.

Page: 17/17



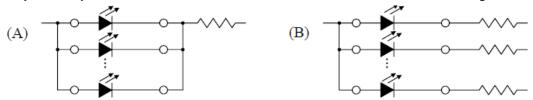
Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

Moisture Proof Package –

- When moisture is absorbed into the SMT package it may vaporize and expand during soldering. There is a possibility that this can cause exfoliation of the contacts and damage to the optical characteristics of the LEDs. For this reason, the moisture proof package is used to keep moisture to a minimum in the package.
- The moisture proof package is made of an aluminum moisture proof bag. A package
 of a moisture absorbent material (silica gel) is inserted into the aluminum moisture
 proof bag. The silica gel changes its color from blue to red as it absorbs moisture.
- Please avoid rapid transitions in ambient temperature, especially in high humidity environments where condensation can occur.

Recommended circuit –

• In designing a circuit, the current through each LED must not exceed the absolute maximum rating specified for each LED. It is recommended to use Circuit B which regulates the current flowing through each LED. In the meanwhile, when driving LEDs with a constant voltage in Circuit A, the current through the LEDs may vary due to the variation in forward voltage (VF) of the LEDs. In the worst case, some LED may be subjected to stresses in excess of the absolute maximum rating.



• This product should be operated in forward bias. A driving circuit must be designed so that the product is not subjected to either forward or reverse voltage while it is off. In particular, if a reverse voltage is continuously applied to the product, such operation can cause migration resulting in LED damage.

Heat Generation –

- Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board, as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.
- The operating current should be decided after considering the ambient maximum temperature of LEDs.

Page: 18/17



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

Static Electricity –

- Static electricity or surge voltage damages the LEDs. It is recommended that a wrist band or an anti-electrostatic glove be used when handling the LEDs.
- All devices, equipment and machinery must be properly grounded. It is recommended that precautions be taken against surge voltage to the equipment that mounts the LEDs.
- When inspecting the final products in which LEDs were assembled, it is recommended to check whether the assembled LEDs are damaged by static electricity or not. It is easy to find static-damaged LEDs by a light-on test or a VF test at a lower current (below 1mA is recommended).
- Damaged LEDs will show some unusual characteristics such as the leak current remarkably increases, the forward voltage becomes lower, or the LEDs do not light at the low current.

Criteria: (VF > 2.0V at IF=0.5mA)

Cleaning –

- It is recommended that isopropyl alcohol be used as a solvent for cleaning the LEDs. When using other solvents, it should be confirmed beforehand whether the solvents will dissolve the package and the resin or not. Freon solvents should not be used to clean the LEDs because of worldwide regulations.
- Do not clean the LEDs by the ultrasonic. When it is absolutely necessary, the influence of ultrasonic cleaning on the LEDs depends on factors such as ultrasonic power and the assembled condition. Before cleaning, a pre-test should be done to confirm whether any damage to the LEDs will occur.

Page: 19/17



Spec. No.	PS-ST-NLANWGA95-K5X
Rev.	D

■ Change story

REV.	Date	Change Description
А	2015.08.20	Original Version
D	B 2016.10.20	Change Package Outline Dimensions
В		2、Change Recommended Soldering Pad Pattern
		Change Electrical / Optical Characteristics
С	2019.12.11	2.Change Absolute Maximum Rating Polarity Notes
		3. Change Luminous Flux Rank Limits
D	2023.03.16	Change address

Page: 20/17